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174/212 Cont.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Chong H. Lee and Reza Asayesh
Application No.: 10/643,276 Confirmation No.: 4060
Filed : August 18, 2003
For : PROGRAMMABLE LOGIC DEVICE WITH HIGH SPEED
SERIAL INTERFACE CIRCUITRY
Group Art Unit : 2819
Examiner : Don P. Le

Hon. Commissioner for Patents
P.O. Box 1450 New York, New York 10020
Alexandria, Virginia 22313-1450 January 18, 2005

TRANSMITTAL LETTER FOR
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


Sir:

Transmitted herewith is a Supplemental Information Disclosure Statement in the above-identified application. This Statement is made before the mailing of a first Office Action after the filing of a Request for Continued Examination under 37 C.F.R. § 1.114.

In accordance with 37 C.F.R. § 1.97, submission of this Statement requires no fee. However, if for any reason, a fee is due, the Director is hereby authorized to charge payment of any additional fees required in connection with the accompanying Third Supplemental Information Disclosure Statement

to Deposit Account No. 06-1075. A duplicate copy of this letter is transmitted herewith.

Respectfully submitted,



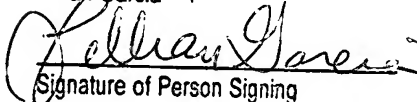
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**THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANTS**

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Sheet 1 of 1

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First Named Inventor	Chong H. Lee
Art Unit	2819
Examiner Name	Don P. Le
Attorney Docket Number	AL-212 Cont.
Confirmation Number	4060

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		5,907,566	05-25-99	Benson et al.	
		6,150,837	11-21-00	Beal et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	
		J.F. Ewen et al. "Single-Chip 1062 Mbaud CMOS Transceiver For Serial Data Communication" IEEE: International Solid-State Circuits Conference, 15 February 1995, pages 32-33, 36.

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